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STATEMENT UNDER 37 CFR 3.73(b)							
Applicant/Patent Owner: Alan J. A. Trainor							
Application No./Patent No.: 10680208 Filed/Issue Date: October 8, 2003							
Entitled: MODULE INTEGRATION INTEGRATED CIRCUITS							
_SiGe Semiconductor Inc, a							
states that it is: 1. the assignee of the entire right, title, and interest; or							
2. an assignee of less than the entire right, title and interest (The extent (by percentage) of its ownership interest is%)							
in the patent application/patent identified above by virtue of either:							
A. An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel <u>014605</u> , Frame <u>0067</u> , or for which a copy thereof is attached.							
OR B. A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as follows:							
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Additional documents in the chain of title are listed on a supplemental sheet.							
As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.							
[NOTE: A separate copy (<i>i.e.</i> , a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. <u>See MPEP 302.08</u>]							
The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee. [4] MAN 4 2 w 3							
Signature Date							
Gary S. Morris 202-481-9988							
Printed or Typed Name Telephone Number							
Authorized Practitioner Title							

This collection of information is required by 37 CFR 3.73(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Practitioner(s) named below (if more than ten patent practitioners are to be named, then a customer number must be used):								
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as attorney(s) or agent(s) to represent the undersigned before the United States Patent and Trademark Office (USPTO) in connection with any and all patent applications assigned only to the undersigned according to the USPTO assignment records or assignment documents attached to this form in accordance with 37 CFR 3.73(b).								
Please change the correspondence address for the application identified in the attached statement under 37 CFR 3.73(b) to:								
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SiGe Semiconductor Inc. 1050 Morrison Drive Suite 100 Ottawa, Ontario Canada K2H 8K7								
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filed in each application in which this form is used. The statement under 37 CFR 3.73(b) may be completed by one of the practitioners appointed in this form if the appointed practitioner is authorized to act on behalf of the assignee,								
and must identify the application in which this Power of Attorney is to be filed.								
SIGNATURE of Assignee of Record The individual whose signature and title is supplied below is authorized to act on behalf of the assignee								
Signature	Steve	Kovnac				Date	December <u>3/</u> , 2007	
Name	Stephen Kovacic					Telephone	6138209244	
Title	Director Legal & IP Management							